

IN THE CLAIMS:

Please cancel claims 10 to 25, 34 and 35, in accordance with the following listing showing the status of all claims in the application.

1. (Original) A method for decoding input data, said method comprising:
  - (a) inputting a first set of symbols and a second set of symbols;
  - (b) decoding the first set of symbols and a feedback set of symbols using a decoder, thereby obtaining a first set of decoded symbols;
  - (c) interleaving the first set of decoded symbols, thereby obtaining a first set of interleaved symbols;
  - (d) decoding the first set of interleaved symbols and the second set of symbols using the decoder, thereby obtaining a second set of decoded symbols;
  - (e) de-interleaving the second set of decoded symbols, thereby obtaining a second set of de-interleaved symbols; and
  - (f) repeating steps (b) through (e) for at least one additional iteration, wherein at each iteration the feedback set of symbols is the second set of de-interleaved symbols obtained during the immediately previous iteration.
2. (Original) A method according to Claim 1, wherein identical hardware is used for decoding in step (b) as for decoding in step (d).

3. (Original) A method according to Claim 1, wherein the decoder is an a posteriori probability decoder.

4. (Original) A method according to Claim 1, wherein interleaving in step (c) comprises:

- (c1) writing the first set of decoded symbols into a buffer, row-by-row;
- (c2) reading from the buffer and writing into a row register a row of the first set of decoded symbols;
- (c3) reading from the row register and writing into the buffer the row of the first set of decoded symbols, so as to perform column interleaving;
- (c4) reading from the buffer and writing into a column register a column of the first set of decoded symbols; and
- (c5) reading from the column register the column of the first set of decoded symbols,

wherein steps (c4) and (c5) together result in row interleaving.

5. (Original) A method according to Claim 4, wherein de-interleaving in step (e) comprises:

- (e1) writing into a second column register the column of the second set of decoded symbols;
- (e2) reading from the second column register and writing into the buffer the column of the second set of decoded symbols, wherein steps (e1) and (e2) together result in row interleaving;

(e3) reading from the buffer and writing into the row register the row of the second set of decoded symbols; and

(e4) reading from the row register and writing into the buffer the row of the second set of decoded symbols, so as to perform column interleaving.

6. (Original) A method according to Claim 5, wherein step (c4) is performed prior to step (e2) for each column in the buffer.

7. (Original) A method according to Claim 1, wherein a single buffer is used for both interleaving in step (c) and de-interleaving in step (e).

8. (Original) A method according to Claim 1, wherein steps (b) through (e) are repeated for a predetermined number of iterations.

9. (Original) A method according to Claim 8, further comprising a step of making a hard decision for each bit position of said input data after completion of said predetermined number of iterations.

10. Canceled.

11. Canceled.

12. Canceled.

13. Canceled.

14. Canceled.

15. Canceled.

16. Canceled.

17. Canceled.

18. Canceled.

19. Canceled.

20. Canceled.

21. Canceled.

22. Canceled.

23. Canceled.

24. Canceled.

25. Canceled.

26. (Original) An apparatus for decoding input data, said apparatus comprising:

input means for inputting coded data;

buffering means for inputting, storing and outputting data;

first register means for storing a portion of the data output from said buffering means;

first read/write means for controlling writing of the data into said first register means and reading of the data out of said first register means so as to change the order of the data;

decoding means for decoding a combination of at least part of the coded data provided by said input means and the data read out of said first register means;

second register means for storing data output by said decoding means;

second read/write means for controlling writing of the data into said second register means and reading of the data out of said second register means so as to change the order of the data, wherein the data read out of said second register means is stored in said buffering means;

third register means coupled to said buffering means; and

third read/write means for transferring the data out of a portion of said buffering means into said third register means and then transferring the same data from said third register means back into said portion of said buffering means, but in a different order,

and for then repeating said transferring steps for different portions of said buffering means.

27. (Original) An apparatus according to Claim 26, wherein data positions in said buffering means are conceptually arranged in columns of data positions and rows of data positions, wherein said first register means and said second register means are for storing a column of data, and wherein said third register means is for storing a row of data.

28. (Original) An apparatus according to Claim 27, wherein said first read/write means reads data from a column in said buffering means prior to when said second read/write means writes data to the same column in said buffering means.

29. (Original) An apparatus according to Claim 26, wherein said third read/write means is activated each time all the data in said buffering means has been rewritten.

30. (Original) An apparatus according to Claim 26, wherein said decoding means comprises an a posteriori probability decoder.

31. (Original) An apparatus according to Claim 26, further comprising means for making a hard decision for each bit position of input data.

32. (Original) An apparatus according to Claim 26, wherein said first register means, said second register means, and said third register means are capable of storing only a fraction of the data that said buffering means is capable of storing.

33. (Original) An apparatus for decoding input data, said apparatus comprising:

- (a) means for inputting a first set of symbols and a second set of symbols;
- (b) means for decoding the first set of symbols and a feedback set of symbols using a decoder, thereby obtaining a first set of decoded symbols;
- (c) means for interleaving the first set of decoded symbols, thereby obtaining a first set of interleaved symbols;
- (d) means for decoding the first set of interleaved symbols and the second set of symbols using the decoder, thereby obtaining a second set of decoded symbols;
- (e) means for de-interleaving the second set of decoded symbols, thereby obtaining a second set of de-interleaved symbols; and
- (f) means for repeating the functionality of means (b) through (e) for at least one additional iteration, wherein at each iteration the feedback set of symbols is the second set of de-interleaved symbols obtained during the immediately previous iteration.

34. Canceled.

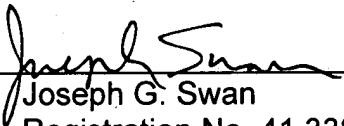
35. Canceled.

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Respectfully submitted,

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